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Simulation and Debug of Mixed Signal Virtual Platforms for Hardware-Software co-development. Vincent Motel, Cadence Design Systems, Inc. (vmotel@cadence.com) Alexandre Roybier, Cadence Design Systems, Inc. (aroybier@cadence.com) Serge Imbert, Cadence Design Systems, Inc. (sergeim@cadence.com)
Abstract—Virtual platforms are often used for high level architecture exploration and hardware-software interactions verification.

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Platform, to help verify its innovative aiWare hardware IP for Neural Network (NN) acceleration for automated driving applications.

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Multi-Core simulation which supports all design languages and constructs and either automatically or manually partitions the design to run in parallel while maintaining a single database for debug and coverage.

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