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(D) +3.2V 34 The input resistance of the circuit shown in the figure assuming an ideal op amp, is (A) R/3 (B)2R/3 (C) R (D) 4R/3 35 In the circuit shown in the figure, the switch S has been in Position 1 for a long Page 39/47

time. It is then moved to Position 2. Assume the Zener diodes to be idea.

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