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and Resistance Matching VLSI Digital Design Flow (Synthesis using Cadence) Using CMOS, finction Implementation (CMOS Designing) What is VLSI?(Explained!!!) UNIT1-INTRO TO VLSI DESIGN Low Power VLSI DesignTesting of VLSI Circuits CMOS VLSI Design of Page 7/35

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Discussion Over the past several years, Silicon CMOS technology has become the dominant fabrication process for relatively high performance and cost effective VLSI circuits.

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1: Circuits & Layout CMOS VLSI Design Slide 45 Gate Layout qLayout can be very time consuming - Design gates to fit together nicely – Build a library of standard cells gStandard cell design methodology - V DD and GND should abut (standard height) -Adjacent gates should satisfy design Page 19/35

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FulestenMOS at bottom and pMOS at
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Lecture 1: Circuits & Layout
To realize complex functions of
multiple input variables, the basic
circuit structures and design
principles developed for NOR and
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NAND can be extended to complex logic gates. The ability to realize complex logic functions, using a small number of transistors is one of the most attractive features of nMOS and CMOS logic circuits.

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The microprocessor is a VLSI device..
Before the introduction of VLSI
technology, most ICs had a limited set
of functions they could perform.

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VLSI design, microprocessors, and computer arithmetic. He holds a dozen patents, is the author of three other books in the field of digital design and three hiking guidebooks, and has designed chips at Sun Microsystems, Intel, Hewlett-Packard, and Evans & Sutherland.

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Complementary MOSFET (CMOS)
technology is widely used today to

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form circuits in numerous and varied applications. Today 's computers, CPUs and cell phones make use of CMOS due to several key advantages.

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