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CHAPTER 1 INTRODUCTION 1.1 47 2.

1.2 1.1 3. 1.3 4. 1.4 1.5 1.6 5. 1.7 6.

Chapter 14 DESIGN FOR

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SOLUTION: (a) First find V_{OL} : the

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transistor operates in linear region when $V_{out} = V_{OL}$, therefore, The reasonable solution is : When $V_{out} = V_{OH}$, consider $V_{IN} = 0$, there is no current through load resistance. When $V_{out} = V_{50\%}$, (b) Transistor is off this case, see figure below.

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Solved > 6.2 Consider switching delays
for 10 fF in a from ...

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Third Edition, By S. M. Kang and Y.
Leblebici, McGraw Hill, 2002 or Forth
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Leblebici; McGraw Hill, 2003. Time &
Location. Cobleigh Hall 632 Tuesday,
Thursday 11:00am - 12:15pm Pre-
Requisites. EE262 - Logic Circuits Lab

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; EE317 - Electronics; Weekly
Schedule

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Behzad Razavi, deals with the analysis
and design of analog CMOS integrated
circuits, emphasizing fundamentals as

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industrial experience of the authors to define a base of electronics instruction for the CMOS chip industry. CMOS Digital Integrated Circuits: A First Course teaches the fundamentals of modern CMOS technology by focusing on central themes and avoiding excessive details.

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