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Site for the book "Circuit Design with VHDL", third edition, written by the author Prof. Volnei A. Pedroni, from Caltech (USA) and UTFPr (Brazil).

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been completely updated and expanded for the third edition.

*Circuit Design with VHDL (The MIT Press) | Volnei A ...*

Pedroni teaches synthesizable VHDL, the kind actually used by electronic design automation tools to make real circuits, and he manages to introduce the language, application, and software tool at the same time. The book is probably too basic for a practitioner wishing to brush up on the language or synthesis, but for the beginner it rocks.

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Prof. Volnei A. Pedroni. The author received his MSc and PhD degrees in Electrical Engineering from the California Institute of Technology (Caltech), with emphasis on VLSI design, analog and digital. He is a Professor of Electronics

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Engineering at Brazil's UTFPR and a regular Visiting Faculty of Electrical Engineering at Caltech. He has done collaboration also with other Institutions, in USA and Italy (he is a Brazilian and Italian citizen).

*About the Author | Circuit Design with VHDL by Volnei A ...*

The book is organized in a clear progression, with the first part covering the circuit level, treating foundations of VHDL and fundamental coding, and the second part covering the system level (units that might be located in a library for code sharing, reuse, and partitioning), expanding upon the earlier chapters to discuss system coding. Part I, "Circuit Design," examines in detail the background and coding techniques of VHDL, including code structure, data types, operators and

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